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Online Inductor Parameters Identification by Small Signal Injection for Sensorless Predictive Current Controlled Boost Converter

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Abstract—In a sensorless predictive current controlled boost converter, parameterizing the inductor plays an important role in controller performance. In this paper, a solution for inductor parameters online identification is investigated. A small signal injection strategy is proposed to create a transient state, and convergence problem of inductance identification in steady state can be avoided. Then a charge balance current observer (CBCO), derived from capacitor current charging balance concept, is adopted to estimate the inductor current for inductance identification. Since inductance is not used in CBCO, current estimation is not affected by inductance identification error. Because of rank-deficient problem, instead of identifying inductor parasitic resistance solely, the inductor equivalent parasitic resistance is derived. By applying it into the conventional current observer for current control loop, the accuracy of current estimation can still be guaranteed since more parasitic effects are included. To improve the accuracy of inductance identification, a load identification method is investigated. Furthermore, the effect of the equivalent series resistance (ESR) of output capacitor on the proposed algorithm is analyzed. Finally, its effectiveness is verified by experimental results.

Index Terms—Sensorless, boost converter, inductor parameters, online identification, small signal injection.

I. INTRODUCTION

The research on current mode control for dc-dc converters have been an important topic for a long time due to their higher response speed and larger loop gain bandwidth than conventional voltage mode control [1]-[8]. However, a precise and high speed current sensor is required to detect the inductor current and this increases the cost. Moreover, its signal processing module introduces delay and noise to the control loop. As an alternative approaches, \( V^2 \) control is studied [9]-[11]. In \( V^2 \) control, equivalent series resistance (ESR) of output capacitor is utilized instead of traditional current sensing resistors. It has the advantages of fast load transient characteristics and is widely applied in buck converters. For digitally controlled boost converters, sensorless predictive current control (SPCC) has become more and more popular, because it inherits the advantages of predictive current control without extra hardware cost. To realize SPCC, the predictive current control (PCC) and sensorless current control need to be combined together. From published literatures on PCC, in [12], Chen proposed an algorithm to eliminate the inductor current disturbance in one switching cycle for peak, average and valley current control modes. Lai further investigated PCC based peak current mode control in [13]. The effectiveness to eliminate the disturbance in limit cycle by PCC with leading edge PWM modulation scheme was verified by theoretical derivation [14]. To realize sensorless current control, a current observer is normally used. The performance of current estimation relies on modeling accuracy of current observer. An accurate current observer model was established for sensorless current control [15]. However, the model is too complex to implement. In [16], an algorithm for average current sensorless control was proposed. With consideration of a number of parasitic parameters, the current estimation is relatively accurate. In [17], to eliminate the voltage steady state error and achieve high accuracy current estimation, a comprehensive compensation strategy was proposed and it can eliminate the effects of component parasitic parameters and nonlinear factors.

With the aforementioned research achievements, the performance of sensorless predictive current controlled boost converters keeps improving all the time. However, there is still a big challenge in producing good performance that relies on accurate component parameters acquisition for model update, because working environments, loading levels and aging effects can cause the component parameters to change over time. A practical solution is to use the online parameter identification to track the parameter variations. There are several publications focusing on power converters online parameter identification [18]-[21]. In [22], a comparison between an algebraic parameter identification algorithm and classical asymptotic observers for the load of a boost converter was carried out. The algebraic algorithm, unlike the asymptotic observers, does not have stability and convergence issues. The algebraic identification algorithm for load and input voltage identification was proposed in [23]. With the identified parameters, the Generalized Proportional Integral indirect adaptive controller can be tuned online accordingly.

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real-time detection technique of the capacitance \((C)\) and its ESR for boost converters was investigated in [24]. Based on the sampling of capacitor current and ripple voltage, the capacitor parameters are obtained by using Kalman filter. Further, Yao proposed an online identification algorithm for output capacitor’s ESR and \(C\) without using any current sensor [25]. Only an output voltage sampling at particular moment in a switching cycle needs to be added. The above literatures provide solutions for the load, capacitance, and its ESR online identification of boost converters.

For SPCC based boost converters, because the current is estimated through a current observer, its performance is even more sensitive to parameter variations. As an essential element in current observer modeling and controller design, the inductance identification with accuracy can affect the current estimation and controller performance. Since no current sensor is utilized, more difficulties such as current estimation accuracy, algorithm convergence, etc. exist in the inductor parameters identification. There has not yet been any publication to investigate online inductance identification for SPCC based boost converters.

There are three main issues to realize accurate inductor parameters identification. One is the inductance identification cannot be executed during steady state, because the inductance calculation result is not convergent. Secondly, if the same model is used for inductance identification and current estimation, the two processes are coupled with each other, leading to the inductance estimation falling into self-iteration status. This means the estimated inductance value remains at its initial setting. The last issue is the inductor current estimation error caused by parasitic parameters which affects the inductance identification. Furthermore, the inductor parasitic resistance couples with other parasitic parameters due to model rank-deficient, so it is hard to identify independently.

Aiming at finding out solutions for aforementioned three issues, a small signal injection scheme is proposed for inductance identification. A low amplitude and short duration disturbance is injected into the voltage reference to build a transient state in which the inductance can be identified. CBCO is introduced for current estimation in the inductance identification process. Since conventional current observer is still used for current estimation in the current control loop, the current estimation can be carried out independently between current control and inductance identification processes. Finally, the inductor equivalent parasitic resistance which consists of the parasitic resistance of the inductor, turn-on resistance of MOSFET, etc. is derived instead of identifying the inductor parasitic resistance itself. Then it is applied to the conventional current observer. With this approach, the accuracy of current estimation can be guaranteed because more accurate parasitic effects are taken into account and the independent inductor parasitic resistance identification is omitted.

The paper is organized as follows. In Section II, three main issues of inductor parameters online identification in SPCC based boost converter are investigated. In Section III, the strategy for inductance identification is proposed at first, i.e. a short and small voltage reference disturbance is applied to create a transient state for inductance identification. Then, CBCO is introduced for current estimation in inductance identification process. With this approach, the current estimation accuracy cannot be affected by inductance identification error. After that, the inductor equivalent parasitic resistance is also derived to guarantee the current estimation accuracy. In addition, a load estimation scheme is investigated in this section for improving inductance estimation accuracy. Since output capacitor ESR is not included in inductor equivalent parasitic resistance, its effect on the proposed algorithm is analyzed in Section IV. Finally, the experimental results and their analysis are given in Section V.

II. THE ISSUES OF INDUCTOR PARAMETERS IDENTIFICATION

The control diagram of SPCC based boost converter is shown in Fig.1. There are two control loops: voltage control loop is the outer loop that generates the reference current, while the inner current control loop applies conventional current observer other than current sensor to obtain the inductor current. With the above configuration, there are three issues for inductor parameters identification. The first one, which is actually not exclusive to SPCC based system, is the non-convergence in inductance value calculation and it stays infinite in steady state. The other two issues are identical in SPCC based system. One is that the current estimation and inductance identification couple with each other, which leads the inductance calculation to a self-iteration state. The other is inductor parasitic resistance \(R_L\) identification. Because of the rank-deficient problem, it cannot be identified independently. In this case, the current estimation accuracy cannot be guaranteed.

![Fig. 1. Structure of conventional sensorless current control for boost converter](image)

A. The issue of inductance estimation in steady state

When the boost converter works in CCM mode, considering the inductor parasitic resistance \(R_L\), inductor current function during the \(k\)th switching cycle in discrete time domain is expressed as

\[
I_L(k+1) = I_L(k) + D'(k) V_O(k) - I_L(k) R_L. \quad (1)
\]

The basic current estimator is also expressed in (1), where \(L\) is the inductance, \(I_L(k)\) represents the inductor current, \(V_O(k)\) is the average output voltage, \(V_{in}(k)\) is the input voltage, \(D'(k)\) is the duty ratio, \(D'(k) = 1 - D(k)\) and \(T\) is the switching period. The inductance can be derived from (2).
\[
L = \frac{V_{in}(k) - D'(k) V_o(k) - I_i(k) R_L T}{I_i(k+1) - I_i(k)}.
\]  
(2)

When system is in steady state, the inductor current in neighbouring two switching cycles stays constant. If \( L \) is identified depending on (2), its calculation result goes infinite which means the identification result is not convergent.

### B. The issue of coupling between current estimation and inductance identification

Since inductor current and inductance have to be estimated and identified by using inductor current function, if (1) is used for current estimation and (2) is used for inductance identification, the two processes coupled with each other lead to \( L \) staying at its initial value. In addition, the inductor current accuracy cannot be high if \( L \) is not updated in real time. To decouple these two processes, a new current observer is necessary for inductance identification.

### C. The issue of inductor parasitic resistance estimation

According to (1) and (2), in order to estimate current and identify inductance accurately, \( R_L \) should be obtained beforehand. In steady state, \( R_L \) can be described as (3) and it also couples with the inductor current.

\[
R_L = \frac{V_{in}(k) - D'(k) V_o(k)}{I_i(k)}
\]  
(3)

If the circuit parasitic parameters such as switch conducting resistance \( R_{DS} \), diode forward conducting resistance \( R_D \) and diode forward conducting voltage \( V_D \) are considered during the system modelling, the inductor current model in turn-on and turn-off status are described in (4) and (5). All these parasitic parameters change along with the environmental and working conditions, so in (4) and (5), together with \( R_L \), these four parasitic parameters are variables. Therefore \( R_L \) cannot be derived based on (4) and (5) due to rank-deficiency. In addition, for digital control system, \( V_o(t) \) is substituted by the sampled output voltage. The measurement errors caused by \( R_C \) and the output voltage measurement noises are brought to the inductor current model, and then the difficulty of \( R_L \) identification can be further increased.

\[
L \frac{dI_i(t)}{dt} = V_{in}(t) - I_i(t) (R_L + R_{DS})
\]  
(4)

\[
L \frac{dI_i(t)}{dt} = V_{in}(t) - \left[ V_o(t) + V_D \right] - I_i(t) (R_L + R_D)
\]  
(5)

### III. THE PROPOSED ONLINE INDUCTOR PARAMETERS IDENTIFICATION ALGORITHM

In this section, an online inductor parameters identification algorithm for peak current mode SPCC based boost converter system is investigated. With the proposed algorithm, the issues mentioned in Section II are solved without adding any extra hardware cost and finally \( L \) and \( R_L \) can be identified accurately. The structure of the boost converter with proposed online inductor parameters estimation algorithm is shown as Fig. 2. The current control loop uses a conventional current observer to realize sensorless predictive peak current control mode. Compared with Fig. 1, the controller parameters and system model are updated online according to the identified inductor parameters \( L \) and \( R_L \) to achieve optimal performance.

**Fig. 2. Control structure of SPCC based boost converter with inductor parameters online estimation.**

In the proposed algorithm, a low amplitude and short duration step signal is added to the voltage reference \( V_{REF} \). With this approach, a transient state is created for inductance identification. With sampled input and output voltages, the average current during turn off period \( I_{AV}(k) \) is derived from the charge balance current observer at first. Then they are used to identify \( L \) and \( R_L \).

For the inductor parameters, temperature, loading levels, and aging effects can cause them to change over time. Since the aging constant is very large, the variations of inductor parameters caused by aging effects are very small in a short time. So the time period of inductor parameters identification should not be set too short, otherwise it will cause a waste of hardware resources. While the temperature constant is much smaller than the aging constant and the load levels may change in any time, a short time period should be set to ensure timely monitoring. Taking these factors into consideration, the voltage reference signal injection is conducted once every second in real application. The injected signal level is low and its duration is short, so this approach does not compromise much of the system performance.

#### A. Inductance identification through small signal injection

The voltage reference signal injection is done at the beginning of a switching cycle. Its duration is 50\( \mu \)s and its amplitude is 3% of designed output voltage. The duration of signal injection is chosen based on the dynamic time required for the inductance identification algorithm. The selection principles of injection amplitude are as follows: 1) the amplitude should be large enough to reduce the influences of the output voltage measurement noises and quantization errors on the accuracy of inductance identification. This can also improve the convergence of the inductance identification algorithm; 2) the disturbance caused by the signal injection will not greatly influence the system performance. If the system has a high resolution AD converter, the injection amplitude can be smaller.

The inductor current during this process is described as Fig. 3. The PWM modulation scheme is “leading edge modulation”. Because \( D(k) \) is calculated in the \((k-1)th\) cycle, the system is still in steady state in the \(kth\) cycle. However, the duty ratio of
(k+1)th cycle changes in order to follow the change of voltage reference and the transient state is created.

![Fig. 3. The inductor current waveform during the signal injection process](image)

During this transient state, the inductance is derived from (2). However, (2) is equivalent to (1). For L identification, (1) should not be used again to avoid the identification process falling into a self-iteration state. According to the charging principle of output capacitor, a new inductor current observer is derived and it is shown in (6).

Since L is not included in (6), the current estimation accuracy cannot be affected by inductance identification, which is the design purpose of this new current observer. In addition, CBCO is proposed for the first time to achieve accurate current estimation for L identification process. Although the current estimation has one switching cycle delay and this delay may cause some oscillation in conventional current estimation, it is very suitable for inductor current estimation. Because L identification process takes several switching cycles to finish, this one cycle delay should not affect it.

\[
C V_O(k+1) - V_O(k) = D(k) I_{av}(k) |D'| - V_O(k) \frac{R}{R},
\]

where \(I_{av}(k) |D'|\) and \(V_O(k)\) are the average current during turn off period and sampled output voltage at the very beginning of the \(k\)th cycle respectively. Then \(I_{av}(k) |D'|\) can be obtained by (7).

\[
I_{av}(k) |D'| = C V_O(k+1) - V_O(k) \frac{T D(k)}{2} + V_O(k) \frac{R D(k)}{2}
\]

In the transient state, peak current \(I_p(k)\) and whole cycle average current \(I_{av}(k)\) can be derived from \(I_{av}(k) |D'|\). They are presented by (8) and (9).

\[
I_p(k) = I_{av}(k) |D' + \frac{M_z(k) D(k) T}{2},
\]

\[
I_{av}(k) = I_{av}(k) |D' + \frac{T}{2} [M_z(k) D(k) |D'| - M_z(k) D(k) D'(k)]
\]

where \(M_z(k)\) is the positive slope of inductor current in the \(k\)th switching cycle, and \(M_z(k)\) is the absolute value of negative slope. They are described in (10) and (11) respectively.

\[
M_z(k) = \frac{V_{in}(k) - I_{av}(k) R_L}{L}
\]

\[
M_z(k) = \frac{V_O(k) - V_{in}(k) + I_{av}(k) R_L}{L}
\]

With (7), (8) and (9), \(I_L\) can be derived in the transient state which is then used for inductor parameters identification.

Next, in order to identify the inductance accurately, a more precise model other than (2) is needed. The relationship between peak inductor currents in adjacent two switching cycles is

\[
I_p(k+1) = I_p(k) + T \left[ D(k) M_z(k) - D'(k) M_z(k) \right].
\]

Substituting (10) and (11) into (12), then

\[
L \frac{I_p(k+1) - I_p(k)}{T} = V_{in}(k) - D'(k) V_O(k) - I_{av}(k) R_L.
\]

(13) is the precise conventional current observer model and it is used in the current control loop.

As shown in Fig. 3, when the signal injection happens, the system still stays in the steady state in the \(k\)th cycle, therefore \(I_p(k+1) = I_p(k)\). So the right side of (13) equals zero.

\[
V_{in}(k) - D'(k) V_O(k) - I_{av}(k) R_L = 0
\]

Substitute (8) into (13) then use (14) for simplification, \(L\) can be identified, the results is represented as \(L_{est}\), shown as (15).

\[
L_{est} = \frac{V_{in}(k) \left( \frac{T D(k + 1)}{D' - \left[ I_{av}(k + 1) \right] D'} \right) - V_O(k) \left( I_{av}(k) \right) R_L}{(2) \left( I_{av}(k + 1) \right) \left( D' - \left[ I_{av}(k) \right] D' \right) T}
\]

The divergence and accuracy issues of inductance estimation can be solved by (15). In (15), \(V_{in}(k)\) and \(V_O(k)\) are sampled, \(I_{av}(k) |D'|\) and \(I_{av}(k)\) are derived from (7) and (9) respectively.

### B. The inductor parasitic resistance identification

The estimation process of \(R_L\) can be carried out in steady state, such as the cycle in which the small signal injection begins. According to (14), \(R_L\) is obtained through

\[
R_L = \frac{V_{in}(k) \left( \frac{T D(k + 1)}{D' - \left[ I_{av}(k + 1) \right] D'} \right) - V_O(k) \left( I_{av}(k) \right) R_L}{I_{av}(k) |D'|}
\]

In the steady state, the average current during the turn-off period is equal to the whole switching cycle average current, so \(I_{av}(k)\) can be replaced by \(I_{av}(k) |D'\). Finally, the identified value of \(R_L\) is named as \(R_{est}\), is expressed as (17). Compared with (16), the computational complexity of (17) is lower because the calculation of (8) and (9) is avoided.

\[
R_{est} = \frac{V_{in}(k) \left( \frac{T D(k + 1)}{D' - \left[ I_{av}(k + 1) \right] D'} \right) - V_O(k) \left( I_{av}(k) \right) R_L}{I_{av}(k) |D'|}
\]

However, the parasitic parameters such as \(R_{DS}, R_D,\) and \(V_D\) can affect the identification result of (17). Considering all the parasitic parameters, \(M_1(k)\) and \(M_2(k)\) can be described as (18) and (19).

\[
M_1(k) = \frac{V_{in}(k) - I_{av}(k) \left( R_L + R_{DS} \right)}{L}
\]

\[
M_2(k) = \frac{V_O(k) - V_{in}(k) + I_{av}(k) \left( R_L + R_D \right)}{L}
\]

Substituting (18) and (19) into (12), then

\[
L \frac{I_p(k+1) - I_p(k)}{T} = V_{in}(k) - I_{av}(k) \left( R_L + R_{DS} \right) + R_D \left( D'(k) - D'(k) \right) \left[ V_O(k) + V_D \right]
\]
Comparing (17) with (20), all the parasitic parameters reflected into \( R_{LEST} \) can be describe in (21). As shown in (21), \( R_{LEST} \) derived from (17) includes not only \( R_L \), but also the effects of \( R_{DS}, \ R_D \) and \( V_D \) on inductor current estimation. This is the equivalent parasitic resistance of the inductor.

\[
R_{LEST} = R_L + R_{DS}D(k) + R_D'D(k) + \frac{V_D'D'(k)}{I_{AV}(k)}
\]  \( (21) \)

The inclusion of other parasitic parameters makes \( R_{LEST} \) deviate from \( R_L \). Substituting (21) into (13), the modified equation is equivalent to (20). This means that the estimated inductor current can be described as (20) after \( R_L \) is replaced by \( R_{LEST} \). Compared with (13), the current estimation is more accurate because more parasitic effects are considered.

C. Load identification

As shown in (7), load \( R \) is required for inductance identification. In order to get accurate identification results, the load variation should be considered. \( R \) needs to be estimated along with the inductance estimation process. Its estimation can be achieved by simply adding an extra point for output voltage sampling in one chosen switching cycle.

![Fig. 4. Output voltage waveform in steady state](image)

Fig. 4 shows the output voltage waveform in steady state. At the beginning of each switching cycle, which is point \( B \), the output voltage is sampled. \( A \) is the added sampling point which is ahead of \( B \) for time \( T_o \) within the previous cycle’s turn-on period. During this turn-on period, the relationship between output voltage and load current is

\[
C \frac{dV_o(t)}{dt} = -\frac{V_o(t)}{R}.
\]  \( (22) \)

The output voltage curve is approximated as a falling linear slope when the switch is on. Then the relationship between the output voltage in \( A \) and \( B \) is derived from integrating both sides of (22).

\[
C \frac{V_B - V_A}{T_o} = -\frac{V_A + V_B}{2R}
\]  \( (23) \)

Then the load value is

\[
R = \frac{T_o(V_A + V_B)}{2C(V_A - V_B)}.
\]  \( (24) \)

Considering the variations of capacitance, \( C \) that exists in (7) and (24) can be tuned by using the online capacitance identification algorithm which is proposed in [25].

D. Algorithm steps summary

![Fig. 5. Procedure of the proposed algorithm](image)

After all the above derivations, the whole process of the proposed algorithm is illustrated as Fig. 5, which is summarized as the following steps:

1) Injecting a small signal into \( V_{REF} \) in the \( k \)th switching cycle and adding an output voltage sampling point in this cycle for the calculation of load.

2) Calculating \( R \) according to (24) in the \( (k+1) \)th switching cycle, then \( I_{AV}(k) \), \( D' \) can be estimated by (7) in this switching cycle.

3) According to the estimated \( I_{AV}(k) \), \( D' \), \( R_{LEST} \) can be obtained in the \( (k+1) \)th switching cycle. \( L_{EST} \) has to be calculated in the \( (k+2) \)th cycle because the system starts to enter a transient state in the \( (k+1) \)th cycle.

4) In the \( (k+2) \)th switching cycle, the \( M_1(k), M_2(k) \) and controller parameters are updated according to \( L_{EST} \) and \( R_{LEST} \).

The proposed algorithm overcomes the aforementioned issues for inductor online parameters identification. With this approach, the system model and controller parameters are updated online and optimal performance can be expected.

IV. ANALYSIS OF ESR EFFECT

Apart from \( R_C \) of the output capacitor, the other parasitic parameters are all included in \( R_{LEST} \). Therefore, any variation in these parasitic parameters can be updated by \( R_{LEST} \) identification. However, since \( R_C \) is not included, its effect on the proposed algorithm should be analyzed.

Because of the effect of \( R_C \), the output voltage is not equal to capacitor voltage. Including \( R_C \), the relationship between average output voltage \( V_o(k) \) and average voltage of capacitor \( V_C(k) \) is

\[
V_o(k) = V_C(k) + I_{C}(k)R_C,
\]  \( (25) \)

where \( I_{C}(k) \) is the average current of capacitor which is equal to the right side of (6). Then

\[
I_{C}(k) = D'(k)I_{AV}(k)\frac{D'V_o(k)}{R}.
\]  \( (26) \)
The capacitor voltage at the beginning of the \( k \)th switching cycle is \( V_C(k) \), its relationship with \( V_o(k) \) is \( V_C(k) = V_o(k) - V_o(k)R_C \). According to the voltage and current characteristics of the output capacitor, and combined with (25) and (26), the accurate relationship between \( V_C(k) \) and inductor current is

\[
C \frac{V_C(k+1) - V_C(k)}{T} = D'(k)I_{av}(k)\left|D' - R - \frac{V_C(k)}{R + R_C}\right.\quad (27)
\]

In order to reduce the derivation complexity, this approximation \( V_C(k) \approx V_C(k) \) can be derived. In addition, \( R_C \) is in the milliohm range which is far lower than load \( R \), so the actual inductor current \( I_{av}(k)\left|D'ight. \) can be obtained from (28).

\[
I_{av}(k)\left|D'\right. = C \frac{V_C(k+1) - V_C(k)}{TD'(k)} + \frac{V_C(k)}{RD'(k)}\quad (28)
\]

The inductor current estimation error is \( \Delta I(k) = I_{av}(k)\left|D' - I_{av}(k)\right|D' \). Comparing (7) with (28), the current estimation error can be derived as (29).

\[
\Delta I(k) = CR_C\frac{V_o(k+1) - V_o(k)}{TRD'(k)} + \frac{V_o(k)R_C}{R^2D'(k)}\quad (29)
\]

According to (15) and (16), the inductor current estimation error can affect the identification accuracy of \( L_{est} \) and \( R_{est} \).

The slopes of the two continuous switching cycles can be regarded as constant because the switching period is relatively short, so \( M_2(k+1) \approx M_2(k) \), then \( V_C(k+1) \approx V_C(k) \). Finally (15) can be simplified as

\[
I_{est} = \frac{V_C(k)}{2[I_{av}(k+1)\left|D' - I_{av}(k)\right]\left|D'\right.}\quad (30)
\]

The actual inductance is \( L' \), and it can be described as (31).

\[
L' = \frac{V_C(k)}{2[I_{av}(k+1)\left|D' - I_{av}(k)\right]\left|D'\right.]}\quad (31)
\]

The identification error of inductance is \( \Delta L = L' - L_{est} \).

Subtracting (30) from (31) and substituting (29), the relative error of inductance estimation is

\[
\frac{\Delta L}{L'} = K_1\frac{V_o(k+1) - V_o(k+2)}{I_{av}(k+1)\left|D' - I_{av}(k)\right|D'} + K_2\frac{D'(k+1) - D'(k)}{I_{av}(k)}\quad (32)
\]

where \( K_1 = CR_C/2TRD'(k) \) and \( K_2 = V_o(k)R_C/R^2D'(k)D'(k+1) \).

As mentioned previously, \( R_C \) is normally very low, so the values of \( K_1 \) and \( K_2 \) are low. In addition, \( V_o(k+1) - V_o(k+2) \) and \( D'(k+1) - D'(k) \) are close to 0, while \( I_{av}(k+1)\left|D' - I_{av}(k)\right|D' \) is relatively large in the transient state. So the value of (32) is almost equal to 0, which means that the effect of \( R_C \) on inductance identification is negligible.

With the similar derivation process for the inductance estimation, the relative error of equivalent parasitic resistance is

\[
\frac{\Delta R_L}{R_L'} = -\frac{\Delta I(k)}{I_{av}(k)\left|D'\right.}\quad (33)
\]

where \( \Delta R_L = R_L' - R_L \) and \( R_L' \) is the actual value of the inductor equivalent parasitic resistance.

The identification of \( R_L \) is done in the steady state. Simplifying (29) with \( V_C(k) = V_C(k+1) \), then substituting it into (33), finally (34) is obtained.

\[
\Delta R_L' = -\frac{V_o(k)}{R_L'}\frac{R_C}{R^2D'(k)I_{av}(k)\left|D'\right.}(k)\quad (34)
\]

According to (34), \( \Delta R_L' / R_L' \approx 0 \), so the effect of \( R_L \) on \( R_L' \) identification is also negligible.

The output voltage measurement noises can also affect the proposed algorithm. For inductor parasitic resistance identification, the influence of the measurement noises is very small. For inductance identification, its accuracy is inversely proportional to the measurement noises. By decreasing \( C \) and increasing \( I_{av}(k+1)\left|D' - I_{av}(k)\right|D' \), the effect of the measurement noises can be reduced. In addition, the common mode rejection circuits and high performance measurement circuit should be designed to reduce the measurement noises. On this basis, the effect of the output voltage measurement noises on inductance estimation is relatively small, which can be verified by the experimental results in section V.

V. EXPERIMENTS

In order to verify the proposed algorithm, a digitally controlled boost converter is built for experimental verification. Its specifications are shown in Table I.

<table>
<thead>
<tr>
<th>TABLE I</th>
<th>SPECIFICATIONS OF THE TESTED BOOST CONVERTER</th>
</tr>
</thead>
<tbody>
<tr>
<td>Input voltage</td>
<td>6V</td>
</tr>
<tr>
<td>Output voltage</td>
<td>12V</td>
</tr>
<tr>
<td>Switching frequency</td>
<td>100kHz</td>
</tr>
</tbody>
</table>

A. Experimental settings

The system hardware contains a control board and a power board. A Texas Instruments digital signal processor (DSP) TMS320F2812 is the core part of control board used to implement the control algorithm.

The power board includes the power stage and signal level shifting circuits. The switching device is an Infineon BSZ110N06NS3 MOSFET. The output capacitor type is Panasonic EEEHZAIJ560P. Chosen diode is NXP PMEG045V050EPD. The specifications for these components are shown in Table II. The switching frequency is 100kHz. For monitoring, the estimated peak current \( I_P \) is output synchronously by a 12-bit DAC TVL5616. The actual inductor current \( i_L \) is measured by a current probe with a resolution of 200mV/A.

For \( R \) identification, the related parameters \( T_o \) is set as 0.8DT. The identification process is carried out every second, and results shown in the Fig.7 are for a period of 60 seconds. All the identified values given in the paper are their average values.

<table>
<thead>
<tr>
<th>TABLE II</th>
<th>SPECIFICATIONS OF HARDWARE PLATFORM</th>
</tr>
</thead>
<tbody>
<tr>
<td>Inductance of the power inductor</td>
<td>28 ( \mu )H</td>
</tr>
<tr>
<td>Inductor winding resistance</td>
<td>50m( \Omega )</td>
</tr>
<tr>
<td>Capacitance of the output capacitor</td>
<td>56( \mu )F</td>
</tr>
<tr>
<td>ESR value of the output capacitor</td>
<td>30( \Omega )</td>
</tr>
<tr>
<td>Load resistance</td>
<td>10( \Omega )</td>
</tr>
<tr>
<td>MOSFET ( R_{DS} )</td>
<td>11m( \Omega )</td>
</tr>
<tr>
<td>Diode forward Voltage</td>
<td>0.42V</td>
</tr>
<tr>
<td>Diode forward resistance</td>
<td>0 m( \Omega )</td>
</tr>
</tbody>
</table>
B. Experiment Results and analysis

1) Inductance identification result

![Image](image-url-1)

Fig. 6. Output voltage waveform when voltage injection happens

Fig. 6 shows both AC and DC components of the output voltage when the pulse signal is injected for inductance identification. As Fig. 6 shows, the voltage rises by 0.32V because of the reference voltage injection. When the injection finishes, the output voltage goes back to 12V. The whole process lasts 200μs. During this period, the inductance identification and control parameters updating are also processed. Output voltage variation is less than 3% of the rated output voltage which means the system can still be judged as steady state during the identification process.

The identification results of \( R \), \( L_{\text{EST}} \) and \( R_{\text{LEST}} \) are shown as Fig. 7a, Fig. 7b and Fig. 7c respectively. As can be seen from the figures, the identified values of \( R \), \( L_{\text{EST}} \) and \( R_{\text{LEST}} \) are 10.06Ω, 26.509μH and 0.138Ω. Compared with their rated values, the identification errors of \( R \) and \( L_{\text{EST}} \) are all less than 6%. Because \( R_{\text{LEST}} \) is only used for the current observer of current control loop, its identification performance can be verified by the current estimation waveforms presented next.

![Image](image-url-2)

(a)

![Image](image-url-3)

(b)

![Image](image-url-4)

(c)

Fig. 7a. Identification results of \( R \) during 60 seconds time, Fig. 7b. Identification results of \( L_{\text{EST}} \) during 60 seconds time, Fig. 7c. Identification results of \( R_{\text{LEST}} \) during 60 seconds time.

For inductor current estimation performance verification, Fig. 8a shows the actual inductor current and estimated inductor peak current without identifying \( R_{\text{LEST}} \). The estimated inductor peak current is 3.32A while actual peak current is 3.02A, with a 0.3A error between them. However, Fig. 8b presents the same signals as Fig. 8a but with \( R_{\text{LEST}} \) identification. It shows that the estimated inductor peak current is 3.07A which is very close to its actual value.

In order to verify the dynamic performance of the proposed algorithm and compare the performance between the systems with the proposed algorithm and without parameters identification, experiments are carried out under load and line voltage disturbance conditions.

2) Experiment with line voltage variations

Fig. 9a and Fig. 9b are the output voltage waveforms for the system with the proposed algorithm and the system without parameters identification respectively when the line voltage jumps from 6V to 7.2V. As Fig. 9a shows, with the proposed algorithm, the output voltage rises by 0.22V and stabilizes within 260μs. While for the system without parameters identification function, as shown in Fig. 9b, the voltage rise is 0.25V (an increase of 14%) and the stabilization time is 600μs which is 131% longer.

![Image](image-url-5)

(a)

![Image](image-url-6)

(b)

Fig. 9a. Output voltage waveform with proposed algorithm when line voltage changes, Fig. 9b. Output voltage waveform without parameters identification when line voltage changes

For the parameters identification results, \( R \), \( L_{\text{EST}} \) and \( R_{\text{LEST}} \) are shown as Fig. 10a, Fig. 10b and Fig. 10c respectively. In the beginning 30s, the line voltage is 6V. It changes to 7.2V at 30.5s and last for another 30s. The identification results are summarized as TABLE III.

<table>
<thead>
<tr>
<th>TABLE III</th>
<th>COMPARISON OF THE PARAMETERS IDENTIFICATION DURING LINE VOLTAGE VARIATIONS</th>
</tr>
</thead>
<tbody>
<tr>
<td>Line Voltage</td>
<td>( R )</td>
</tr>
<tr>
<td>6V</td>
<td>10.06Ω</td>
</tr>
<tr>
<td>7.2V</td>
<td>10.028Ω</td>
</tr>
</tbody>
</table>

The results presented in TABLE III suggest that the identification of \( R \) and \( L_{\text{EST}} \) maintains its high accuracy despite the variations in line voltage. With line voltage variations, the...
system stabilizes in a new state, while the inductor current and duty cycle are changed. According to (21), the value of $R_{\text{EST}}$ should deviate from 0.138Ω, as shown in TABLE III. To further verify the performance of $R_{\text{EST}}$ identification, Fig.11 shows the actual inductor current and estimated inductor current with $R_{\text{EST}}$ identification after the change of line voltage. The actual peak current and estimated inductor peak current are 2.55A and 2.59A, respectively. The estimation error is very small, which indicates that the estimated peak current can be effectively corrected by $R_{\text{EST}}$ identification even though line voltage changes.

However, for the system without parameters identification, as shown in Fig.12b, the voltage drops to 10.75V and takes 600µs to stabilize (131% longer than proposed algorithm).

![Fig. 12a. Output voltage waveform with proposed algorithm when load changes, Fig. 12b. Output voltage waveform without parameters identification when load changes](image)

$R$, $L_{\text{EST}}$ and $R_{\text{EST}}$ identification results are shown as Fig. 13a, Fig. 13b and Fig. 13c respectively. In the beginning 30s, the load is 10Ω. It changes to 5Ω at 30.5s and last for another 30s. The identification results are summarized as TABLE IV.

![Fig. 13a. Identification results of $R$ during 60 seconds time under load variations, Fig. 13b. Identification results of $L_{\text{EST}}$ during 60 seconds time under load variations, Fig. 13c. Identification results of $R_{\text{EST}}$ during 60 seconds time under load variations.](image)

<table>
<thead>
<tr>
<th>TABLE IV</th>
<th>COMPARISON OF THE PARAMETERS IDENTIFICATION DURING LOAD VARIATIONS</th>
</tr>
</thead>
<tbody>
<tr>
<td>Load Value</td>
<td>$10\Omega$</td>
</tr>
<tr>
<td>$R$</td>
<td>10.06Ω</td>
</tr>
<tr>
<td>$L_{\text{EST}}$</td>
<td>26.509µH</td>
</tr>
<tr>
<td>$R_{\text{EST}}$</td>
<td>0.138Ω</td>
</tr>
</tbody>
</table>

As can be seen from the results in TABLE IV, the load identification can follow the actual load variations while the inductance identification maintains its high level of accuracy despite the variations of load value. The deviation of $R_{\text{EST}}$
identification is caused by the variations of the inductor current and duty cycle in steady state. $R_{\text{EST}}$ identification performance can be verified by the current estimation waveforms shown in Fig.14. The estimated inductor peak current is 5.65A which is very close to its actual value (5.56A). It indicates that $R_{\text{EST}}$ identification is also accurate even though load changes.

![Fig. 14. Estimated peak current and actual current waveforms with $R_{\text{EST}}$ identification after load changes](image)

As the experimental results show, even the system is under sudden load and line voltage disturbances, with proposed algorithm it will still be able to quickly respond and track to a new steady state. In addition, the parameters identification algorithm shows good robustness.

VI. CONCLUSION

Three issues of inductor parameters online identification of SPCC based boost converter have been solved in this paper. First, by using the small signal injection method, convergent issue of inductance identification in steady state is solved. Then, a CBCO is proposed to overcome the coupling issue. Finally, the inductor equivalent parasitic resistance is derived to guarantee the current estimation accuracy. With the above strategy, the inductor parameters online identification is realized and maintains good accuracy in various working conditions. On this basis, the control parameters are also optimized according to the updating of the parameters identification. Experimental results show that the proposed identification strategy is accurate and effective, and it should have good potentials in various practical applications.

REFERENCES


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