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Characterization of CdTe Photovoltaic Devices Passivated Using Hydrogen Plasma

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Abstract — Thin-film polycrystalline CdTe photovoltaic devices were studied using electrical and material characterization methods to understand the effects of hydrogen plasma passivation treatment. Devices were fabricated using sublimation and were exposed to hydrogen plasma for 10, 20 and 30 minutes. Current density vs voltage measurements were performed to measure the performance of the devices. Capacitance vs voltage graphs showed that dopants are active and the device behaved like a CdCl\(_2\) passivated device. Microscopic characterization was performed using SEM and (S)TEM that showed larger grains and more homogenous film coverage as compared to films without passivation suggesting grain growth during H\(_2\) passivation.

I. INTRODUCTION

Hydrogen plasma passivation has been extensively studied for semiconductor materials such as GaAs, AlGaAs and amorphous as well as crystalline silicon. It is understood to cause surface modification in GaAs\textsuperscript{[1]} wafers and passivation of deep level defects in molecular beam epitaxial (MBE) GaAs\textsuperscript{[2]}. Hydrogen passivation is also used to passivate point defects such as dangling bonds in crystalline silicon\textsuperscript{[3]} and improve passivation of interface between amorphous and crystalline silicon\textsuperscript{[4]}.

These studies suggest that hydrogen introduction using plasma passivation process has a significant impact on semiconductor properties. Some studies have also been performed to understand shallow donor neutralization in n-type indium doped CdTe\textsuperscript{[5]}. Photoluminescence characterization of p-type undoped CdTe shows hydrogen introduction within CdTe film suggesting hydrogen actively plays a role in passivation of defects in CdTe as in case of other semiconductor materials\textsuperscript{[6]}. Since CdTe is now a prominent photovoltaic technology and forms a considerable portion of commercial photovoltaic installations, it is important to understand the effect of hydrogen plasma passivation on CdTe photovoltaic devices. CdTe is one of the most economical and commercially viable photovoltaic technologies and further improvement in efficiency would pave way for reduction in manufacturing cost of CdTe photovoltaic modules.

In this study, CdS/CdTe photovoltaic devices were fabricated sublimation of CdS and CdTe. Hydrogen plasma was generated in-situ without break of vacuum after CdTe deposition. Device performance and efficiency were measured for devices that were passivated using hydrogen plasma and compared against devices fabricated without passivation. SEM and (S)TEM images of the film surface and film cross-section were collected respectively to understand the effect of hydrogen plasma exposure on grain structure. EDS maps were not collected since EDS would not be able to detect presence of hydrogen. Capacitance vs voltage and capacitance vs frequency measurements were performed to understand the effects of exposure to hydrogen plasma on electrical behavior of the device.

II. FABRICATION AND CHARACTERIZATION METHODS

The CdS and CdTe thin films were deposited on NSG TEC 10 soda lime glass that was coated with fluorine-doped tin oxide (FTO), a transparent conducting oxide (TCO). Deposition of these films was performed in a continuous inline sublimation tool optimized at Colorado State University for fabrication of thin-film CdTe photovoltaic devices. The load-lock section of the sublimation tool had a protruding plasma source that was used to expose the films to hydrogen plasma after deposition of CdTe films. Thickness of CdS film was \(~130\) nm while CdTe absorber layer thickness was measured to be \(~2.4\) \(\mu\)m. Flow of hydrogen gas was controlled using a mechanical valve and the pressure in the plasma source was maintained at 800 mTorr. The electrical parameters used to ignite hydrogen plasma were controlled using a Glassman High Voltage power controller and the parameters were maintained at 15 mA current and 450 V voltage. The films were exposed to plasma for 10 and 25 minutes for different substrates.
Thereafter, the films were heated to ~140°C and CuCl was deposited on the film surface for 110 seconds. This was followed by 220 seconds of annealing at 220°C. This process step was performed to form a Cu back contact. Carbon and nickel paint in polymer binder were then sprayed on these films to form the back electrode. Figure 1 shows the schematic of this device structure. The cells were then delineated using a mask and bead blasting to fabricate 25 small scale devices on the substrate. The devices had an area of ~0.65 cm².

For electrical characterization, open-circuit voltage \( [V_{oc}] \), short-circuit current \( [J_{sc}] \), fill-factor and percentage efficiency data were collected using J-V measurements.

In-depth microstructure characterization of the CdTe film was carried out using Transmission Electron Microscopy (TEM) and High Resolution Transmission Electron Microscopy (HRTEM). TEM samples were prepared using Focused Ion Beam (FIB) milling using dual beam FEI Nova 600 Nanolab. A standard in-situ lift out method was used to prepare the cross-section sample through the film stack into the glass substrate. A platinum overlay was deposited on the top of the film to define the area of interest on surface of the sample, homogenize the final thinning of the samples and to avoid damage to the CdTe film surface from the ion beam. STEM bright-field images and high resolution TEM images were collected using a FEI Tecnai F20 (S)TEM operating at 200 kV.

Capacitance-Voltage characteristics were obtained using Kyesight E4990A Impedance Analyzer. The sample bias was swept from -2V to 1V at 20mV step, the capacitance measurements were taken using 500kHz frequency.

### III. Electrical Characterization

As can be observed from current density vs voltage measurements (figure 1), as the films are exposed to hydrogen plasma for longer periods of time there is an improvement in short-circuit current. However, best fill-factor of 58.2% and open-circuit voltage of 549 mV is seen for 10 minutes of passivation time. Longer passivation time for these films show reduction in fill-factor as well as open-circuit voltage. This may be caused by surface damage induced by long exposure to hydrogen plasma leading. Damage to CdTe surface at long passivation times requires to be verified using SEM.

As deposited CdS/CdTe hetero structures not activated using CdCl₂ did not provide response. When the material was H₂ passivated the C-V response showed characteristics typical for a working solar cell. Mott-Schottky characteristics of reference cell and H₂ passivated CdTe are shown in figure 2. The \( V_{bi} \) of H₂ passivated material was higher (~0.67V) than for working solar cell (~0.62V).

![Fig. 2. Mott-Schotky characteristics of devices with and without hydrogen plasma passivation](image)

![Fig. 3. Doping profiles extracted from Mott-Schottky characteristics for a device with and without hydrogen plasma passivation](image)
Doping profiles were extracted from the C-V curves. The H$_2$ treated device showed $\sim4.2 \times 10^{14}$ cm$^{-3}$, the working cell showed $\sim7.3 \times 10^{14}$ cm$^{-3}$. Calculated doping profiles are plotted in figure 3.

IV. MATERIALS CHARACTERIZATION

Scanning electron microscopy (SEM) was used to study the surface of CdTe films after exposure to hydrogen plasma passivation while scanning transmission electron microscopy (STEM) was performed to study the cross-section of these CdS devices. SEM images of the surface showed a more homogenous coverage of CdTe grains with minimal voids after hydrogen plasma passivation (figure 4). These features were similar to the films that are passivated using CdCl$_2$ on similar films.

STEM cross-section images (figure 5) showed stacking faults present in CdTe grains. The grains appeared to small near the interface of CdS and CdTe. These are typical features observed in CdTe grains not passivated using CdCl$_2$. The grain boundaries appeared to be distinct and there were no voids visible in the film. The growth of CdTe was also conformal on CdS. There were no visible signs of material diffusion and the CdS/CdTe interface also appeared to be distinct and abrupt.

V. CONCLUSIONS

Current density vs voltage measurements show that hydrogen passivation has a positive effect on CdS/CdTe device performance. C-F and C-V measurements also suggest that these devices have characteristics of working devices. However, cross-section electron image shows presence of stacking faults that persists post hydrogen plasma passivation treatment. This suggests hydrogen plasma exposure may be responsible for affecting other point defects such as dangling bonds as well as may be passivating the surface that results in improvement of device performance.

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