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Transmission Through Optically Generated Inductive Grid Arrays

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Abstract—A technique is developed for generating pseudo-metallic plasma inductive grid arrays within a semiconductor wafer. The induced plasma elements are defined by a surface impedance which is discussed. Results of an investigation into the transmission properties of a variety of array designs between 16 and 40 GHz are presented. Measurements are performed on a silicon wafer illuminated by an external optical source via a negative image mask.

Index Terms—Arrays, frequency selective surfaces, optical control of microwaves.

I. INTRODUCTION

O PTICAL control of the behavior of the substrate medium of periodic arrays and frequency selective surfaces (FSS) can be achieved by changing the microwave properties of the substrate [1]. The passband of an array of aperture dipoles printed onto silicon can be switched on or off by varying the (complex) dielectric properties of the semiconductor under photoexcitation. The photoconductive properties of silicon are such that an electron–hole pair plasma is produced, and when there is sufficient concentration, it behaves like a conductor. This property of silicon is well known and has been utilized previously in the investigation of leaky wave radiation from dielectric slab waveguides [2], [3], control of Bragg gratings in a dielectric waveguide [4], reflection properties of photoinduced strip gratings [5], optically controlling a silicon coplanar waveguide [6] and, more recently, generation of a singular antenna element using a light tank [7]. As wafers increase in size, printing an antenna array and operating it at microwave frequencies becomes more manageable. Dynamic tuning offers additional and alternative degrees of freedom in the functionality of a system, wherein adaptive and/or reconfigurable systems could result. Tuning with the use of optical signals has attractions because of the many salient features they offer; fast switching speeds, electromagnetic compatibility (EMC), integration, size and weight. A way in which tuning can be enhanced is to electronically imprint an array in the semiconductor and using a variable pattern optical source to alter its microwave response. We have shown theoretically that plasma arrays can be formed, by selectively doping the semiconductor with impurities [8], as well as in a thin semiconductor layer on a supporting sapphire substrate [9].

In this paper, we generate an inductive grid array by fixing an optical mask between a light source and a single wafer, as shown in Fig. 1. A uniform light illumination will generate the plasma only under the transparent areas of the mask. By selecting different mask patterns the array dimensions can be changed. Both theoretical and experimental results are presented whereby the transmission properties are examined for a variety of array designs between 16 and 40 GHz. High resistivity, (typically >6 kΩ·cm) silicon has been used here as the semiconductor substrate. In the mathematical model the plasma is represented as a surface impedance of uniform thickness. An outline of the analysis steps is given together with a derivation of the surface impedance representing the plasma grid array.

II. ANALYSIS

Changes in the dielectric material properties can be achieved by means of an optical source of variable intensity. The underlying principle is the photoillumination of the semiconductor substrate so that electron–hole pairs are generated, thus producing a plasma. Here, we assume that the plasma thickness remains constant while the plasma density varies. Following the geometry shown in Fig. 1, the array is represented by the photoinduced plasma elements whose thickness is $S_p$, and complex relative permittivity $\varepsilon_{r}^p$, (given in the Appendix). $\varepsilon_{r}^p$ is a function of the microwave frequency and the plasma
density \( N \). These elements lie on the \( x-y \) plane and can be characterized by the surface impedance, which varies according to \( \varepsilon_{Si} \). This impedance is taken into account in the integral equation analysis below. The remainder of the dielectric is unexcited (dark) silicon of thickness \( S_{d} \), and relative dielectric constant \( (\varepsilon_{r}) \), which is assumed to be real. The grids on the mask are oriented along \( y \), and have a periodicity \( D \) and a width \( W \).

Here, we adopt the analysis for periodic arrays of infinite extent, where Floquet modes \( (\Psi_{pq}) \) represent the tangential electric and magnetic fields. The procedure follows that given in [10], with the exception of the inclusion of the surface impedance. The latter is derived for normal incidence only.

For propagation along the positive \( x \) axis, the tangential electric and magnetic field expansions are generally formed to include transmitted and reflected waves. In the dark dielectric region, these are given as follows:

\[
E(r, z) = \sum_{pq} (T_{mpq}e^{-j\beta_{pq}z} + R_{mpq}e^{+j\beta_{pq}z})\Psi_{mpq}(r)
\]

\[
H(r, z) = \sum_{pq} \eta_{mpq}(T_{mpq}e^{-j\beta_{pq}z} - R_{mpq}e^{+j\beta_{pq}z})\frac{\varepsilon}{\varepsilon_{r}} x \Psi_{mpq}(r)
\]

where \( T_{mpq} \) and \( R_{mpq} \) are the modal transmission and reflection coefficients respectively. \( \eta_{mpq} \) is the modal admittance, \( pq \) are the modal indices and \( m = 1,2 \) stands for TM and TE modes, respectively.

The propagation constant is

\[
\beta_{pq} = k_{0}(\varepsilon_{r} - k_{x}^{2} - k_{y}^{2})
\]

where \( k_{x} \) and \( k_{y} \) are the tangential wave vector components, and \( k_{0} \) is the freespace wave-number. \( k_{x} \) and \( k_{y} \) depend on the array periodicity, modal indices and angles of the plane wave incidence [10]. Applying the standard electromagnetic boundary conditions, a relationship between \( T_{mpq} \) and \( R_{mpq} \) is obtained:

\[
\frac{R_{mpq}}{T_{mpq}} = \left( \frac{\eta_{m}\varepsilon}{\eta_{pq}\varepsilon_{r} + \eta_{pq}} \right) e^{-2j\beta_{pq}S_{d}}.
\]

In addition, the modal reflection coefficient is expressed in terms of the spectrum of the unknown current \( J \). For this derivation see [10, eq. (2.47)]. Enforcing the condition that the total scattered and incident field \( \mathbf{E}^{\text{inc}} = Z_{sp}\mathbf{J} \), where \( Z_{sp} \) is the surface impedance, we derive the electric field integral equation, which is given by

\[
\sum_{pq} C_{\text{inc}}^{	ext{scat}} T_{mpq}^{\text{scat}}\Psi_{pq}(r) - Z_{sp}\mathbf{J} = -\sum_{m=1} C_{\text{inc}}^{	ext{scat}} T_{mpq}^{\text{inc}}\Psi_{mpq}(r)
\]

where \( \mathbf{J} \) is the Fourier transform of \( J \). The right-hand side represents the incident field and the \( C \)'s are terms involving the substrate parameters, [10]. This equation is reduced, using the method of moments, into a matrix form by expanding the unknown currents as a set of entire domain bases. The array is generated by the formation of the electric current \( J \) as the surface impedance approaches zero. We have assumed that there are no normal currents flowing down the plasma elements. Once the currents have been estimated, the copolar transmission coefficient can then be calculated, [10]. Under a variable light intensity the skin depth varies only in the elements. At low density levels, the imaginary part of the refractive index is small, therefore, the skin depth is very large. On the other hand, as the plasma becomes more dense the skin depth decreases to values smaller than the plasma thickness. Where the impedance is zero, the array has all its elements fully conducting. As the impedance increases the surface conductance decreases and the array begins to behave in a lossy manner, and eventually at very small plasma concentrations it vanishes altogether.

Assuming that there is no diffusion into the silicon substrate and the plasma thickness is smaller than the wavelength the surface impedance can be calculated from the source-free Maxwell–Ampere equation [11]. For normal incidence

\[
Z_{sp} = \frac{E_{o}}{J_{oc}} \Omega / \square
\]

where \( E_{o}(= T_{o} + R_{o}) \) is the tangential electric field in the absence of scatterers \( (p = q = 0) \) at \( z = 0 \). \( J_{oc} \) is the total surface current per unit width of plasma in the \( y \)-direction and is derived from the integration of the effective current density, \( J_{ed} \). \( J_{ed} \) consists of both the effective conduction and effective displacement currents and is equal to the tangential magnetic field at the surface of the plasma medium [12]. Thus, in the absence of sources:

\[
J_{ed} = \sigma(T_{o}e^{-j\beta_{o}z} + R_{o}e^{+j\beta_{o}z})
\]

where \( \sigma = j\omega \varepsilon_{r} \varepsilon_{Si} \).

Integrating (6) over \( S_{p} \) yields \( J_{oc} \). Substituting the latter into (5) and making use of (3) (in the absence of scatterers), the following equation for the plasma impedance is derived:

\[
Z_{sp}^{Si} = Z_{o} \frac{\sinh(jk_{0}n_{Si}^{Si}S_{p}) + n_{Si}^{Si}\cosh(jk_{0}n_{Si}^{Si}S_{p})}{n_{Si}^{Si}(\sinh(jk_{0}n_{Si}^{Si}S_{p}) + n_{Si}^{Si}\sinh(jk_{0}n_{Si}^{Si}S_{p}) - 1)}
\]

where \( Z_{o} \) is the impedance in vacuum and the complex refractive index \( n_{Si}^{Si}(= \sqrt{\varepsilon_{Si}}) \) is shown in Fig. 9.

The surface impedance takes into account all possible scenarios and also covers the special cases, referred to as penetrable and impenetrable, usually considered in conventional printed element arrays [13]. The surface impedance is a function of the concentration, that exists only on the area occupied by the elements. Since the optical excitation will alter the complex refractive index of silicon, any state will be attainable, from dielectric to good conductor inclusive. Fig. 2 shows the impedance for different plasma concentrations. At low concentration, there is high negative reactance, the magnitude of which decreases as the plasma thickness increases, whereas the resistance remains small. At the merging point of the real and imaginary parts of the refractive index \( n_{Si}^{Si}(= \sqrt{\varepsilon_{Si}}) \) is shown in Fig. 9.

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As the plasma thickness increases the resistance decreases. This decrease has some bearing on the amount of plasma concentration required to generate the array, and is discussed in the results below.

III. EXPERIMENTAL SETUP

The array is generated on silicon wafers, which are discs of 100mm diameter and thickness 330 µm. The discs are of a high resistivity (typically >6 kΩ·cm). This is a critical property of the wafers and means that in the dark state the silicon acts principally like a dielectric. A low resistivity implies more metallic behavior, which is the property we are inducing by means of the illumination, so it is important for the dark silicon to possess a high resistivity in order that its behavior contrasts with that of the generated plasma regions.

A negative image mask is used in conjunction with a Xenon strobe lamp to generate the plasma regions on the wafer (Fig. 1). When the light is incident upon the mask it penetrates only the transparent portions into the silicon thereby establishing the plasma distribution as a negative image of the mask. A Xenon source is used because it has a peak in its spectrum around 980 nm and the absorption coefficient of silicon is low at this wavelength while the penetration depth is high. This leads to good conditions for the plasma generation. The array image on the mask has periodicity $D$, and the strips are of width $W$. In an ideal situation the dimensions of the plasma grid would be identical to those of the mask, but for several reasons this is often not the case. Hence, the periodicity and width of the plasma strips are denoted $D_p$ and $W_p$, respectively. The light passing through the mask will experience a degree of diffraction from the pattern and diffusion in the silicon so that the actual plasma array is not likely to be sharply defined in practice, although for clarity it is shown as so in Fig. 1 where the plasma depth, $S_p$, represents the penetration into the silicon of the incident photons. A further remark on this point is that while assessing the transmission properties of the grid structures, the light cannot be normally incident on the wafer because the source will interfere with the path of the transmitted field.

Fig. 3 shows the measurement setup and highlights the issue of making transmission measurements while illuminating the wafer. The absorbing screen is rotated slightly to present an incidence angle of about $10^\circ$ to the electromagnetic field from the transmit antenna. Behind the screen, the lamp is placed so that the optimum trade off is reached between incident light intensity on the wafer, uniform illumination of the wafer and interference free propagation for the transmitted fields. The Xenon strobe lamp has a maximum discharge of 140J and a flash duration of about 6 ms with a maximum of 10 flash/s.

IV. PLANE-WAVE TRANSMISSION

Results have been obtained for a variety of grid dimensions and some representative comparisons between measurement and theory are presented in this section. In all cases the mask is mounted on the wafer by using a very thin layer of a spray adhesive. The wafer and mask are then secured to the absorbing frame which is aligned directly with the boresight from transmit to receive antennas. The antennas are placed such that the wafer is illuminated by a plane wave, being in the far field of the two antennas. In the absence of the mask and glue, the wafer has a response typical of that for a dielectric with $\varepsilon_r$ of 11.8, including the losses due to mismatch between the air and dielectric at these frequencies. Owing to ambiguities that may exist in the dark state of the silicon, glue and mask, we have normalized the results with respect to the dark state transmission, including the mask and glue. The wafer exhibits an insertion loss of around 2.5 dB at 16 GHz, increasing to around 4 dB at 40 GHz. The output power from the RF source was set at about 3 mW, although this is not critical as the results are normalized.

Figs. 4–6 show comparisons of theoretical and measured data for three different grid arrays, depicting clearly the inductive effect at lower frequencies. For all the theoretical plots we have used a single modal index of 13 (i.e., $|a| = 13, q = 0$) since only one dimensional array is considered, together with
four entire domain basis functions to represent the unknown current. It was assumed that the plasma penetrated half the thickness of the wafer (i.e., $S_p = 165 \ \mu m$) and that the remaining $165 \ \mu m$ was pure silicon with $\varepsilon_r = 11.8$. In all cases the carrier density was taken to be $10^{17}$ cm$^{-3}$. It can be seen that for all cases considered here there is a discrepancy between $W$ and $W_p$, where $W_p$ is the width of the plasma strips needed to closely match the measured results with the theory. The combination of $N, W_p, S_p,$ and $S_d$ was deduced from the agreement between theoretical and measured results but the photoinduced plasma may be influenced by the optical absorption depth and/or diffusion length, that may infer a lower plasma density. Although the absorption depth in the spectrum of our light source is less than $100 \ \mu m$, the chosen $S_p$ may be viewed as an effective penetration depth; a combination of optical absorption depth and carrier diffusion. In addition, our model allows only canonical, three-dimensional blocks to represent the plasma elements. The incident photons will be diffracted to a certain extent by the black regions of the mask, and will, therefore, not all penetrate normally, and some will diffuse to outside the extent of the transparent region of the mask. Further, the number of photon–electron collisions taking place make it highly probable that even those photons that are normally incident will not continue in this direction for long. The random nature of the collisions, the incident light intensity and carrier lifetime will also have a bearing on the exact dimensions and extent of the plasma generated. Nevertheless, we have achieved good agreement with the measured data using the assumptions already stated. Namely: $N = 10^{17}$ cm$^{-3}, S_p = 165 \ \mu m$ and $W_p$ as stated in the legend of Figs. 4–6. We have also plotted theoretical curves for values of $W_p$ slightly above and below the optimum value and presented them on the respective graphs. The effect of the change in strip width is not surprising and is about the same as would be observed for a conventional copper inductive grid. The crucial point to note from these results comes from Fig. 4, where the periodicity is just large enough (5 mm) to reveal the effects of the grating lobe region at around 40 GHz, that further verifies that we have produced a photoinduced plasma inductive grid in the silicon slab.

V. INFLUENCE OF PLASMA DENSITY ON TRANSMITTED AND DISSIPATED POWER

The level of generated electron–hole pairs or plasma density $N$ is a measure of how metallic the silicon has become when illuminated. We have performed a study to determine the effect of $N$ on the transmitted power and the power dissipated in the plasma region, for various combinations of plasma and dielectric thicknesses. The mathematical model handles discrete layers of varying, definable properties and in order to match our measured results we adopted the scheme that the plasma penetrated halfway through the wafers, i.e., $S_d = S_p = 165 \ \mu m$. Fig. 7(a) and (b) shows theoretical results for transmission coefficient and dissipated power (within the wafer). The dimensions of the grid are $D = 5$ mm and $W = 2$ mm.

From these graphs it is clear that when $N = 10^{20}$ cm$^{-3}$ the plasma is almost totally metallic. There is little loss at 34...
GHz and more significantly, virtually no dissipative loss. We assumed in these calculations that \( N = 10^{27} \) cm\(^{-3}\) and this has been born out by the results above because the transmission coefficient is coincident with that when the plasma is nearly metallic. The interesting aspect of these results is the effect on dissipative loss when the silicon is in transition from dielectric to metallic. When \( N = 10^{25} \) cm\(^{-3}\) there is very little dissipative loss because the region defined as plasma is essentially silicon but the transmission properties seem to indicate that the whole structure acts as a dielectric grating with a deep null which is dependant on the periodicity. As \( N \) is increased, the plasma becomes very lossy and affects the transmission properties of the wafer to such an extent that, for instance, at 24 GHz with \( N = 10^{25} \) cm\(^{-3}\), 48% of the incident power is dissipated in the silicon. This decreases with frequency for a given \( N \) and becomes close to zero across the range when the wafer is metallic.

For comparison purposes we looked at the situation where \( S_p = 30 \) μm and \( S_d = 165 \) μm to establish the influence of a thinner plasma layer on the same dielectric. Fig. 8(a) and (b) shows the theoretical results for transmitted and dissipated power. The thinner plasma layer has a clear effect on all the transmission responses apart from that when \( N = 10^{20} \) cm\(^{-3}\). This is due to the fact that at this density the thickness has little effect on performance because the plasma is metallic at any thickness. At lower densities the effect is greater because the plasma is in an indistinct region between dielectric and conductor. Our choice of \( N = 10^{17} \) cm\(^{-3}\) exhibits a bit more dissipative loss at this thickness and the transmission coefficient is reasonably stable although the centre frequency of the passband has moved up in frequency by about 1 GHz. Whereas it was coincident with the transmission coefficient for \( N = 10^{20} \) cm\(^{-3}\) it has now become slightly lossy at the passband and shifted up in frequency. This demonstrates that \( 10^{17} \) cm\(^{-3}\) does not represent pure metallic behavior.
In fact the thickness of the dielectric behind the plasma will largely define the frequency of the passband, provided \( N \) is large enough, if the periodicity and the width are fixed. The assumption of \( S_p \) to be 165 \( \mu m \) has been established to be valid because provided the plasma density is high enough then \( S_p \) is not critical to the frequency of operation. The plasma thickness becomes crucial, however, when the plasma is between dielectric and metallic and is essentially a layer of lossy material. In practice there would not be clearly defined demarcation between dielectric and plasma but almost certainly a “plasma gradient” from one side of the wafer to the other.

An important point to make is recognizing the impact of the grating lobes on the level of dissipated power. In Figs. 7 and 8, the dissipated power tends to decrease as the frequency rises toward 40 GHz. This is due to the fact the grating lobes have not encroached into our frequency range and is independent of the value of \( N \).

VI. CONCLUSION

We have demonstrated a basic technique for optically generating arrays of periodic grids as a pseudometallc, electron–hole plasma region within a semiconductor wafer. The wafers used were high resistivity silicon to give dielectric–like dark behavior, in order to contrast with the generated plasma. The theory of the work is based on the characterization of the plasma by means of its surface impedance, which in turn is dependent on the complex permittivity. We have demonstrated theoretically, and verified experimentally, the generation of grid arrays of several different dimensions and shown that the array performance is largely determined by the density of the plasma, \( N \). Plasma densities of \( 10^{17} \text{ cm}^{-3} \) are necessary to achieve the near metallic state required to resonate as a conventional conductor. At lower densities the silicon becomes, essentially, a lossy medium in which up to 48% of incident power is dissipated. Tied in with this we have highlighted the effects of the plasma thickness and shown that this is not critical to performance provided the density is high enough, although the thickness of any dielectric support layers needs to be considered as this will primarily determine passband frequency for a given plasma. Results have been presented in the frequency range 16–40 GHz to confirm that grids have been optically cast onto the wafers using negative image masks of the desired pattern.

This work forms the basis of a longer term program to develop a fully integrated technique for the formation of resonant periodic arrays of plasma, within silicon, which may be in the form of cells of discrete elements and comparable in dynamic characteristics to a conventional FSS.

APPENDIX

COMPLEX RELATIVE PERMITTIVITY OF Si

The semiconductor complex relative permittivity containing electron–hole pairs is expressed as a sum of two, electrons \((e)\) and holes \((h)\) dependent terms

\[
\varepsilon_{rSi} = \varepsilon_u - \sum_{i=e,h} \frac{\omega_0^2}{(2\pi f)^2 + \nu_i^2} \left(1 + j\frac{\nu_i}{2\pi f}\right) \tag{A1}
\]

where \(\omega_0^2 = (N\nu_e^2/\varepsilon_0m_e)\) is the plasma’s angular frequency, \(\varepsilon_u\) is the dark dielectric constant, \(\nu_i\) is the collision angular frequency, \(m_i\) is the effective mass of the carrier, \(q\) is the electronic charge and \(\varepsilon_0\) is the permittivity of freespace. For the silicon substrate the following values have been used in the computer model: \(\varepsilon_u = 11.8, \nu_e = 4.53 \times 10^{12} \text{ s}^{-1}, \nu_h = 7.71 \times 10^{12} \text{ s}^{-1}, m_e = 0.259m_0, m_h = 0.38m_0, m_0\) is the free electronic mass.

Fig. 9 shows the refractive index of silicon versus \( N \) at 40 GHz. Typically, at low plasma concentration the real part initially remains almost constant and then increases in such a way that (near \( 1.0 \times 10^{19} \text{ cm}^{-3} \)) it coalesces with the imaginary part reaching a value of about 10. Subsequently, they both increase with the same rate and have similar values. They reach a value of about 1000 at a concentration of \( 1.0 \times 10^{20} \text{ cm}^{-3} \), at which the semiconductor has reached its near metallic state.

REFERENCES


He is currently a Research Associate in the Wireless Communications Group, Department of Electronic and Electrical Engineering at Loughborough University, investigating the forming of discrete arrays by optical means. From 1996 to 1997, he was a Research Assistant in the Department of Electronics at the University of York, York, U.K., where he worked on the EU ACTS project FRANS (Fibre Radio ATM Networks and Services) investigating high-performance patch antenna arrays, feeder networks and filters. His research interests include frequency selective surfaces, microstrip circuits, and optical control of microwaves.

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Between 1988 and 1995, he worked for the GEC-Marconi Hirst Research Centre, Wembley, U.K., becoming manager of their Long Range Research Laboratory in 1994. In 1995, he was appointed Professor of Electronic Device Engineering in the Department of Electronic and Electrical Engineering, Loughborough University, Loughborough, U.K. In 1997, he was appointed Head of Department. The author or co-author of over 60 technical articles, he has worked on topics ranging from high-frequency GaAs devices for radar applications, through fundamental studies of SiGe heterostructures for high-speed electronics, to signal processing in radar, communications, and imaging systems.